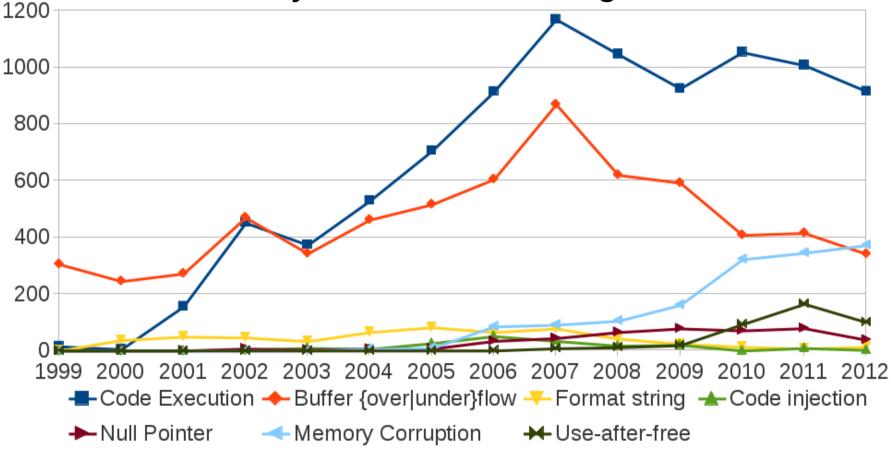


SoK: Eternal War in Memory Laszlo Szekeres, Mathias Payer, Tao Wei, and Dawn Song In: Oakland '14

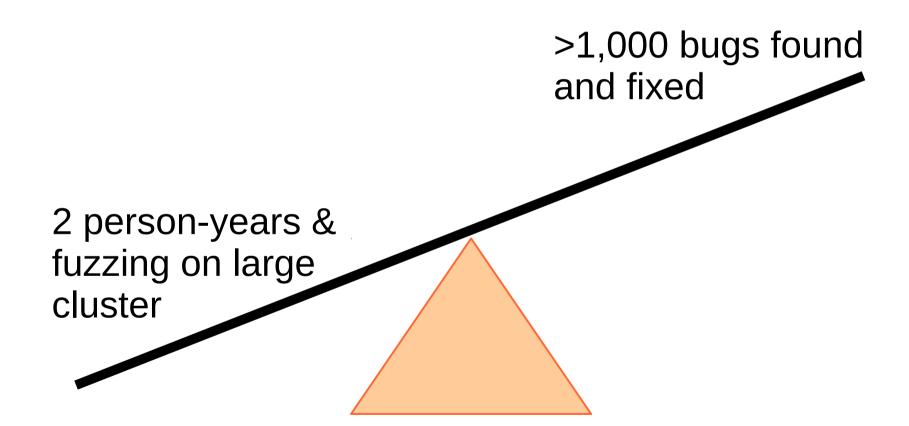
Presenter: Mathias Payer, EPFL http://hexhive.github.io

Memory attacks: an ongoing war

Vulnerability classes according to CVE



FFmpeg and a thousand fixes

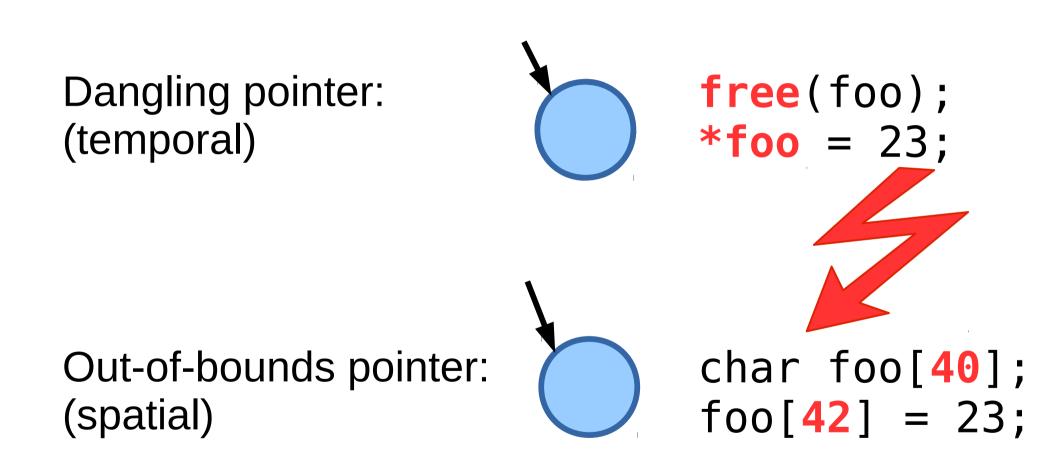


Software is unsafe and insecure

- Low-level languages (C/C++) trade type safety and memory safety for performance
 - Programmer responsible for all checks
- Large set of legacy and new applications written in C / C++ prone to memory bugs
- Too many bugs to find and fix manually
 - Protect integrity through safe runtime system

A Model for Memory Corruption

Memory (un-)safety: invalid deref.



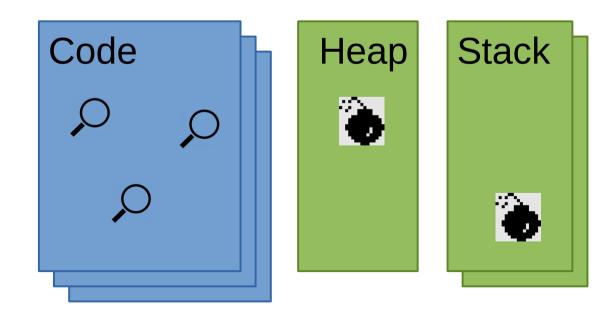
Violation iff: pointer is read, written, or freed

Type Confusion

```
vtable*? 	Dptr
                        Bptr
                                   b
class B {
  int b;
                                  C?
};
class D: B {
  int c;
                               vtable*
  virtual void d() {}
                          B
                                   b
                                           D
};
...
                                   C
B *Bptr = new B;
D *Dptr = static cast<D*>B;
Dptr->c = 0x43; // Type confusion!
Dptr->d(); // Type confusion!
```

Attack scenario: code reuse

- Find addresses of gadgets
- Force memory corruption to set up attack
- Leverage gadgets for code-reuse attack
- (Fall back to code injection)



Benign control-flow

```
void vuln(char *u1) {
   // strlen(u1) < MAX ?
   char tmp[MAX];
   strcpy(tmp, u1);
   ...
}</pre>
```

```
vuln(&exploit);
```

tmp[MAX]

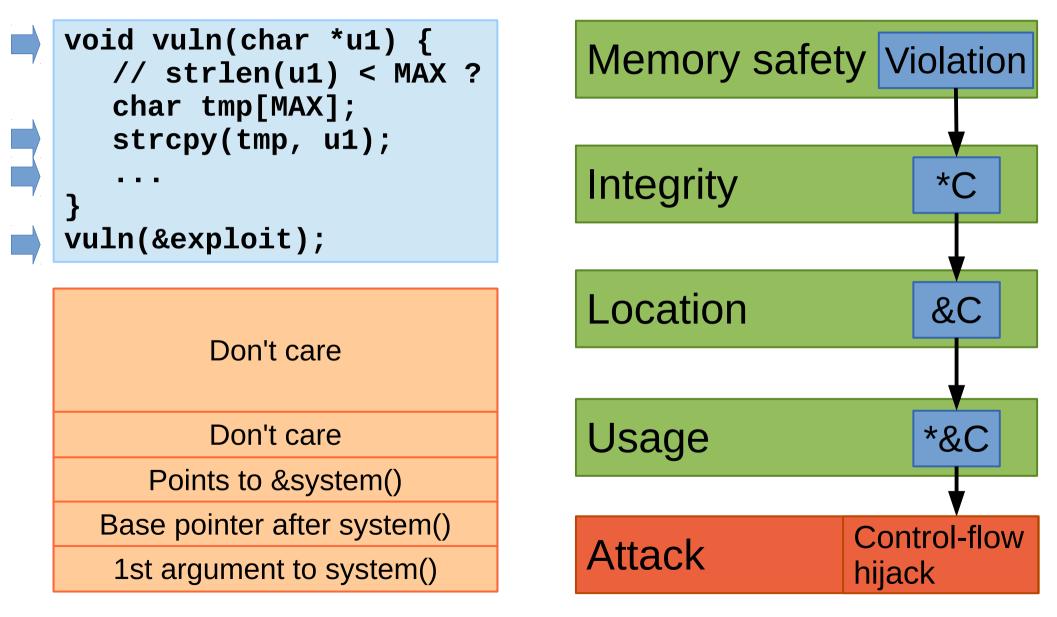
Saved base pointer

Return address

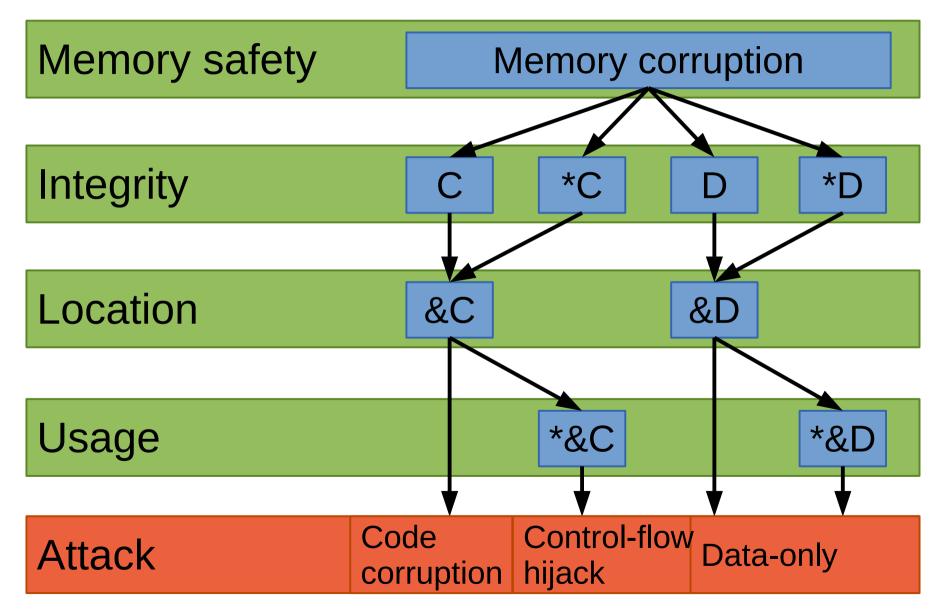
1st argument: *u1

Next stack frame

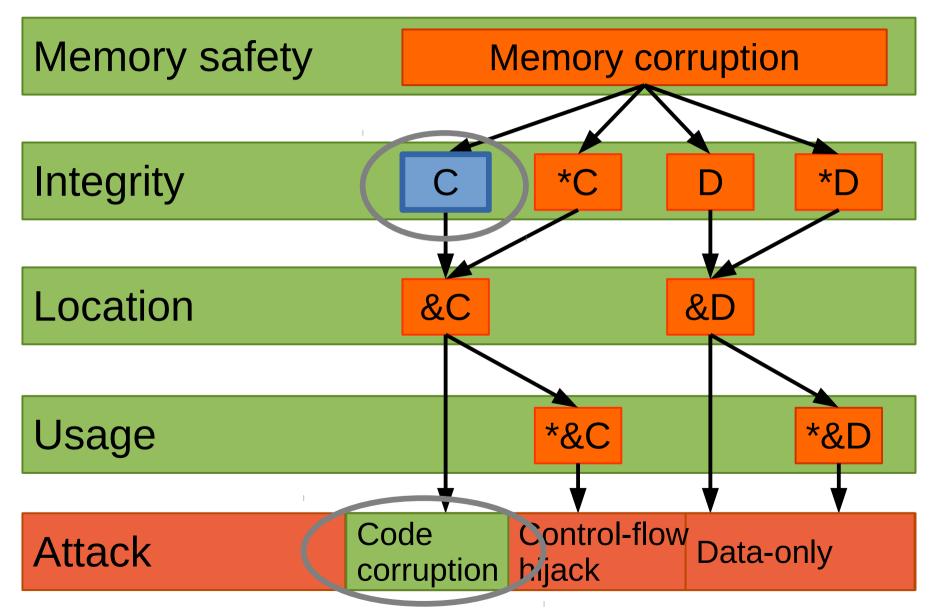
Control-flow hijack attack



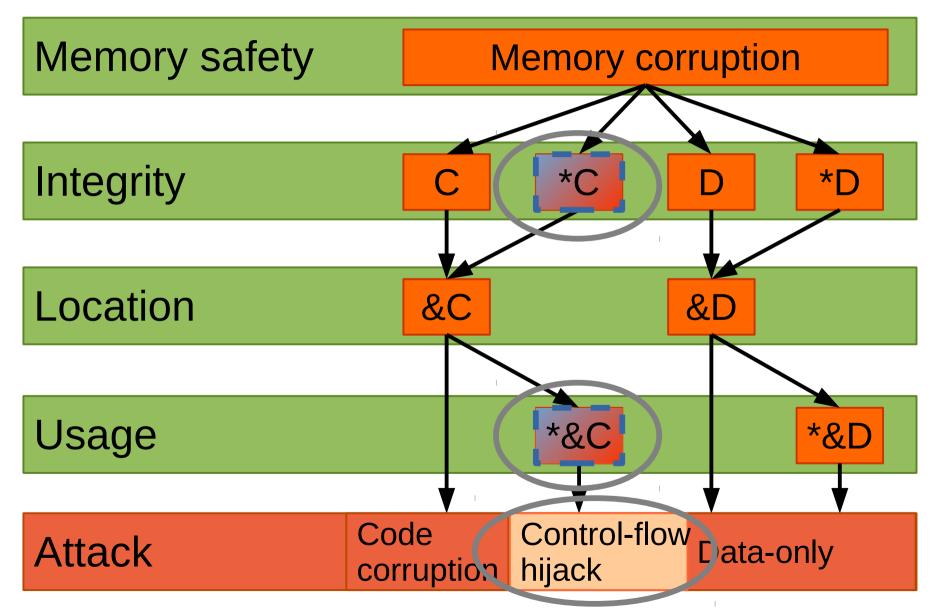
Model for memory attacks



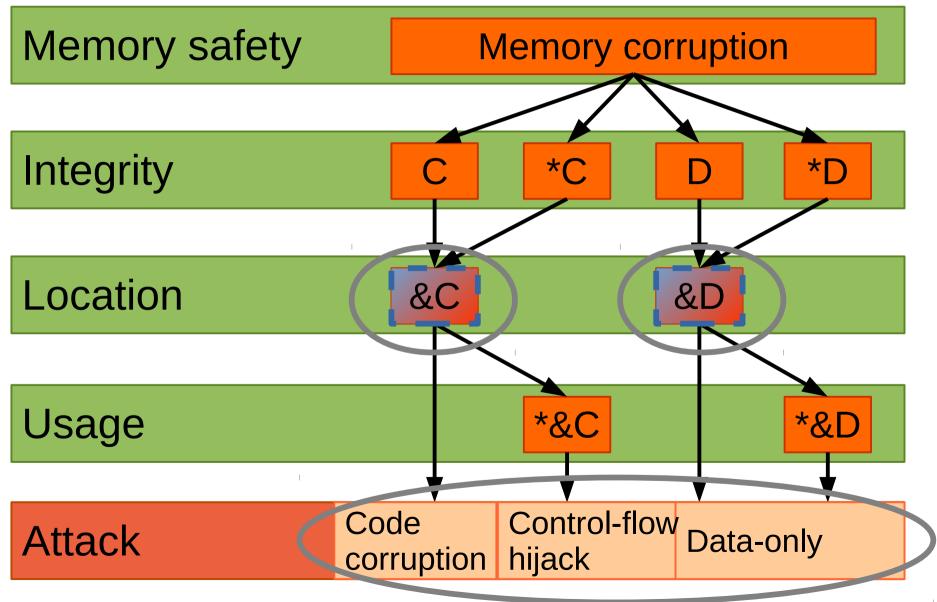
Data execution prevention



Stack canaries and SEH

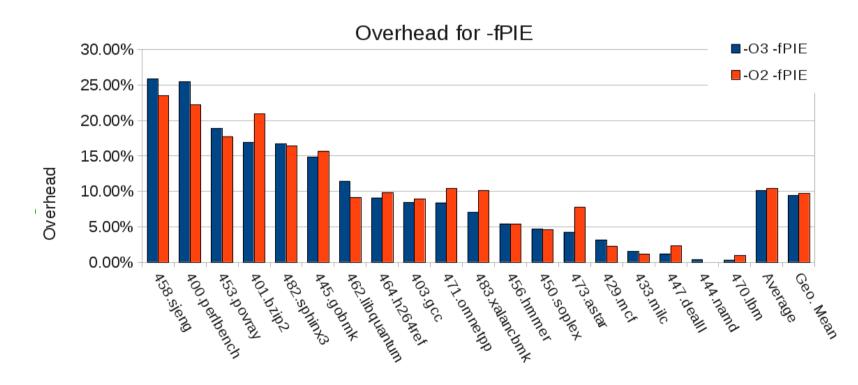


Address space layout random.

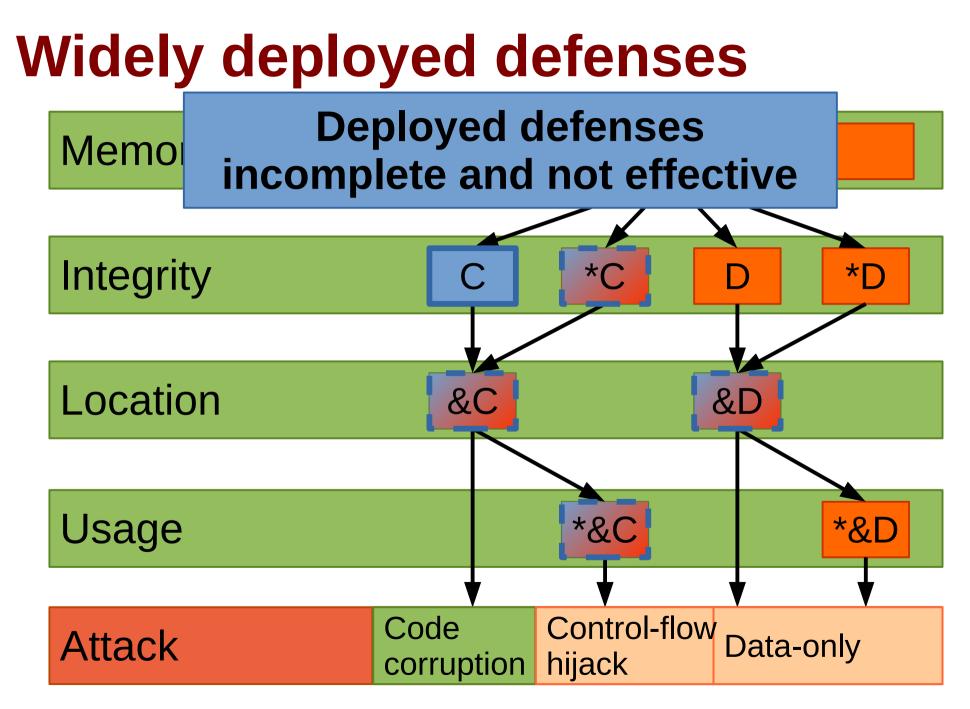


ASLR: Performance overhead

- ASLR uses one register for PIC / ASLR code
 - Performance degradation on x86

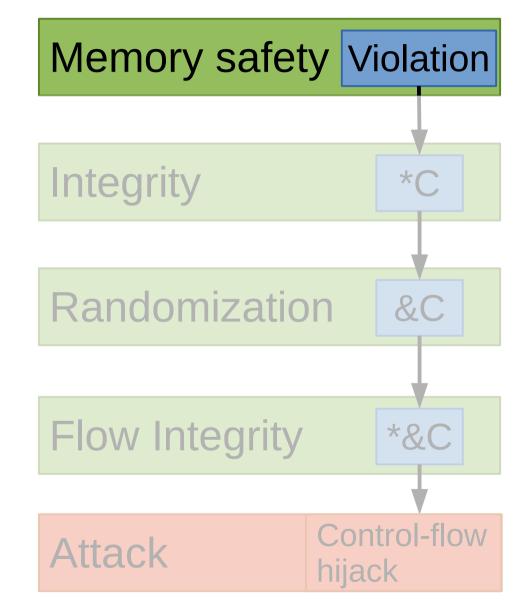


SPEC CPU2006 benchmark



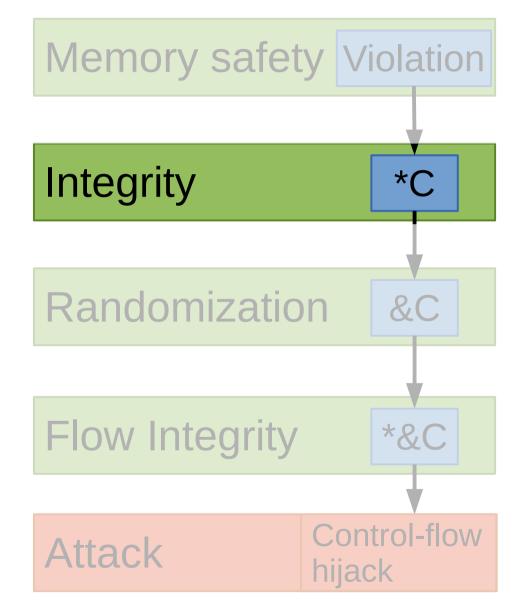
Stop memory corruption

- Safe dialects of C/C++: CCured, Cyclone
- Retrofit on C/C++: SoftBounds+CETS
- Rewrite in safe language: Java/C#

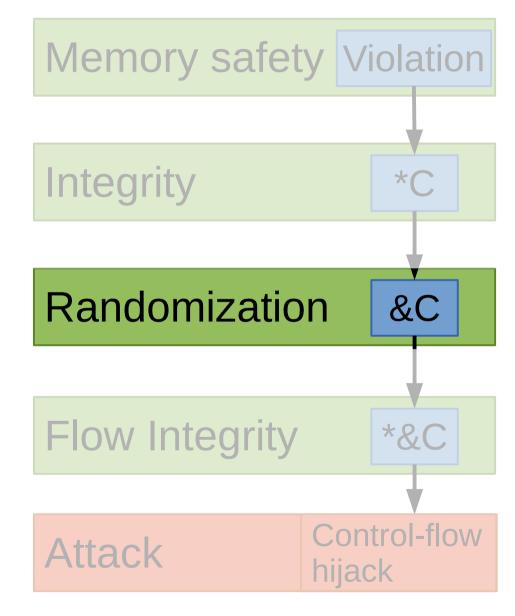


Enforce integrity of reads/writes

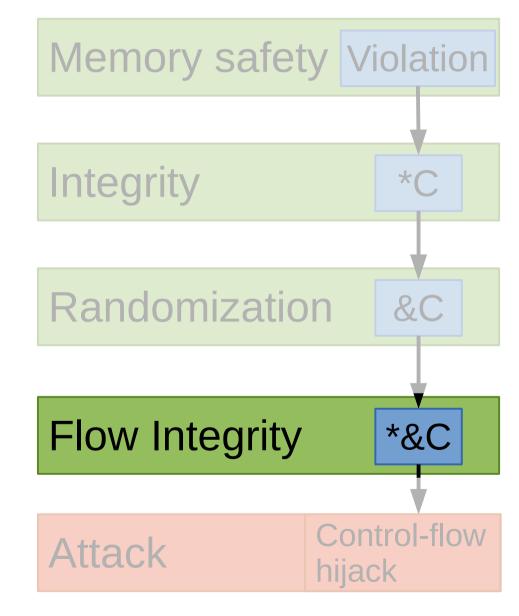
- Write Integrity Testing
- (DEP and W^X for code)



- Probabilistic defenses
- Randomize locations, code, data, or pointer values



- Protect control transfers
- Data-flow integrity
- Control-flow integrity



Model for memory attacks

- Model allows reasoning and classification
 - Classify security policies and defense mechanisms
 - Reason about power of attacks
- Identify properties that enable wide adoption
 - Low overhead is key (<10%)
 - Compatibility with legacy code and source code
 - Protection against class(es) of attacks

Conclusion

Conclusion

- Low level languages are here to stay
 - We need protection against memory vulnerabilities
 - Enforce performance, protection, compatibility
- Mitigate control-flow hijack attacks
 - Secure execution platform for legacy code
 - Code-pointer integrity for source code
- Future directions: strong policies for data
 - Protect from other attack vectors